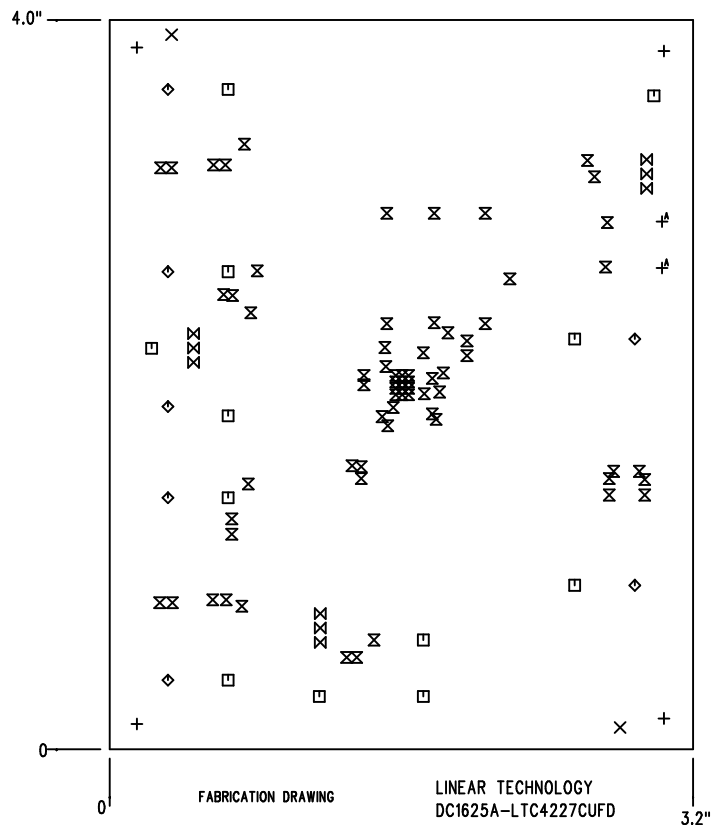


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPR	DATE
-	2	2ND PROTOTYPE	VLADIMIR O.	04-27-10



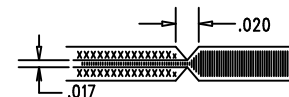
SIZE	QTY	SYM	PLATED	TOL
0.185	4	+	NO	+/-0.003
0.07	2	X	NO	+/-0.003
0.094	12	□	YES	+/-0.003
0.205	7	◇	YES	+/-0.003
0.012	69	⊗	YES	+/-0.003
0.031	9	⊠	YES	+/-0.003
0.065	2	⊕ ^A	YES	+/-0.003


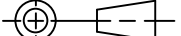
LINEAR TECHNOLOGY
DC1625A-LTC4227CUFD
DUAL IDEAL DIODE AND
SINGLE HOT SWAP CONTROLLER
DATE: 04-27-10

- LAYER 1 (COMPONENT SIDE)
- LAYER 2
- LAYER 3
- LAYER 4 (SOLDER SIDE)

NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -EPOXY FIBERGLASS, NEMA GRADE FR-4
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS
AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH
HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003"
IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB:



UNLESS OTHERWISE SPECIFIED		APPROVALS		<div> LINEAR TECHNOLOGY</div> <div>1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY</div>	
<div>DIMENSIONS ARE IN INCHES</div> <div>TOLERANCES:</div> <div>0.XX" = ±0.01"</div> <div>0.XXX" = ±0.005"</div> <div>INTERPRET DIM AND TOL PER ASME Y14.5M-1994</div> <div>THIRD ANGLE PROJECTION</div> <div></div>		PCB DES.	AK		
		APP ENG.	VLADIMIR O.		
		TITLE: FABRICATION DRAWING			
		DUAL IDEAL DIODE AND SINGLE HOT SWAP CONTROLLER			
		SIZE	IC NO.		REV
		N/A	LTC4227CUFD-X DEMO CIRCUIT 1625A		2
		SCALE = NONE		FILENAME:	DC1625A-2.PCB
				SHT 1 OF 1	